THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Makoto KAKEGAWA

Application No.: 09/960,501   ATTENTION

Confirmation No.: 7247   CERTIFICATE OF CORRECTION

Filed: September 24, 2001   BRANCH

U.S. Patent No.: 6,757,880 B2   Issued: June 29, 2004

For: LOGICAL CIRCUIT DESIGNING DEVICE, LOGICAL CIRCUIT DESIGNING METHOD, STORAGE MEDIUM AND PROGRAM

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

NOV 2 4 2004

of Correction

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentee respectfully requests that a Certificate of Correction be issued in the subject patent, pursuant to 35 USC § 254 and 37 CFR § 1.322, to correct the error shown on the attached Form PTO-1050. A check in the amount of $100 to cover the cost of the Certificate is attached.

One or more of the errors shown on the attached Form PTO-1050 is or are within the responsibility of the undersigned; each thereof is of a clerical or typographical nature or of minor character and occurred in good faith, and the correction thereof is consistent with the prosecution record.

11/22/2004 NNGUYEN2 00000127 6757880

01 FC:1811   100.00 OP

Respectfully submitted,

STAAS & HALSEY LLP

Date: _____11/18/4_____

By: _____
J. Randall Beckers
Registration No. 30,358

1201 New York Ave, N.W., Suite 700
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501

3 0 NOV 2004

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,757,880

DATED : June 29, 2004

INVENTOR(S) : Makoto KAKEGAWA

Col. 15, line 7, change "pf" to --of--.

MAILING ADDRESS OF SENDER:
STAAS & HALSEY LLP
1201 New York Ave., N.W.
Suite 700
Washington, DC 20005

PATENT NO. 6,757,880

No. of add'l copies
@ 50¢ per page

⟹

3 0 NOV 2004